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(21) International Application Number: PCT/US96/13443 (22) International Filing Date: 20 August 1996 (20.08.96) (30) Priority Data: 08/517,578 21 August 1995 (21.08.95) US (71) Applicant: RODEL, INC. [US/US]; 451 Bellevue Road, Newark, DE 19713 (US). (72) Inventor: ROBERTS, John, V., H.; 17 West Country Lane, Newark, DE 19702 (US). (74) Agent: BENSON, Kenneth, A.; P.O. Box 191, Chatham, PA 19318 (US).		(81) Designated States: CN, JP, KR, SG, European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i> <i>Before the expiration of the time limit for amending the</i> <i>claims and to be republished in the event of the receipt of</i> <i>amendments.</i>
(54) Title: POLISHING PADS (57) Abstract A pad is provided for use on a machine for the polishing of silicon wafers which allows the use of optical detection of the wafer surface condition as the wafer is being polished. This is accomplished by constructing the entire pad or a portion thereof out of a solid uniform polymer sheet with no intrinsic ability to absorb or transport slurry particles and which is transparent to the light beam being used to detect the wafer surface condition by optical methods. Polymers which are transparent to light having a wavelength within the range of 190 to 3500 nanometers are suitable for the construction of these pads.		

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POLISHING PADS

BACKGROUND OF THE INVENTION

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Technical Field

This invention relates to polishing pads used for creating a smooth, ultra-flat surface on such items as glass, semiconductors, dielectric/metal composites and integrated circuits. It particularly relates to the bulk
10 structure of such pads and their ability to allow optical in-situ end point detection during the polishing or planarization process.

Background Art

It is desirable to effect planarization of integrated circuit structures in the form of semiconductor wafers during the manufacture of multilayer
15 integrated circuits. The planarization must be very precise, providing a wafer surface that varies from a given plane by as little as a fraction of a micron. This is usually accomplished by CMP, chemical-mechanical polishing, on an apparatus most often comprised of a rotating table, usually circular, onto which is affixed a polishing pad, a wafer carrier which presses
20 the wafer flatly onto the polishing pad, and a means of supplying chemicals and abrasives to the polishing pad in the form of a slurry. Apparatus for polishing thin, flat semiconductor wafers are well known in the art. Such planarization apparatus are manufactured by IPEC Planar, Strausbaugh Manufacturing and the SpeedFam Corporation among others.

25 A particular problem encountered when planarizing semiconductor wafers on such apparatus is the determination that a wafer has been polished to the desired degree of flatness. Most end-point detection methods shown in the art rely on the change in the surface structure of the wafer as an overlying

layer is removed. Thus flatness is not measured, but is only considered secondary to removal of the overlying layer. In U.S. Patent 5,036,015 it is the change in friction between the wafer and the polishing pad which indicates an end-point. In U.S. Patent 5,240,552 the thickness of the wafer is measured by the analysis of reflected acoustic waves. In U.S. Patent 5,337,015 special electrodes underneath the polishing pad along with an electrically grounded polishing table and the use of a conductive slurry allows the dielectric layer thickness to be measured. These devices for in-situ measurement of thickness are very complicated and rely on specialized electronic circuitry to accomplish the task. Most often, instead of using a complicated in-situ method, wafers are removed from the polishing apparatus and flatness is measured using a spectroscopic device to measure the oxide film thickness. Usually, the wafer is taken out of the polishing operation before the expected end point is reached so that excess polishing does not occur. Then the wafer is reinserted into the polishing machine for polishing to the desired endpoint.

U.S. Patent 5,081,796 shows a method and an apparatus for carrying the wafer while on the polishing machine out over the edge of the polishing pad so that a rapid method of measuring the oxide layer, such as laser interferometry, can be used on the underside of the wafer. This method has the disadvantage of removing part of the wafer from the polishing process at any given time so that the wafer does not receive uniform polishing at all times. This is also true for the optical end point detection method in semiconductor planarizing polishing processes shown in U.S. Patent 5,413,941. It would be very desirable to have a machine upon which such laser light measurements could be employed while the wafer is continuously under total polishing conditions.

SUMMARY OF THE INVENTION

A pad is provided for use on a machine for the polishing of silicon wafers which allows the use of optical detection of the wafer surface condition as it is being polished. This accomplished by constructing the entire pad or a portion thereof out of a solid uniform polymer sheet with no intrinsic ability to absorb or transport slurry particles and which is transparent to the light beam being used to detect the wafer surface condition by optical methods. Polymers which are transparent to light having a wavelength within the range of 190 to 3500 nanometers are suitable for the construction of these pads.

DETAILED DESCRIPTION OF THE INVENTION

There are polishing pads now being used for the polishing of silicon wafers which are made of from a solid uniform polymer sheet. These are described in U.S. Patent 5,489,233 which is made part of this specification by reference. The solid uniform polymer sheet has no intrinsic ability to absorb or transport slurry particles. This inability to absorb or transport slurry particles distinguishes the bulk properties of polishing pads made from the solid uniform polymer sheet from the bulk properties of any prior art polishing pads. All prior art pads have a bulk structure which is made up of fibers, contains pores as a result of either being filled with microballoons or blown during manufacturing, or are filled with abrasive. Although the prior art pads might be made from a solid polymer, they have a bulk structure which is opaque to a beam of light because they are not a uniform structure and will severely scatter any light beam directed onto them. The surface of the polymer sheet useful for the present invention may be provided with both macrogrooves and microgrooves which transform the solid uniform sheet

into an excellent polishing pad. As pointed out in the referenced U.S. Patent 5,489,233, such pads can be made out of any solid uniform polymer including polyurethanes, acrylics, polycarbonates, nylons and polyesters. Since all of these can be made of a polymer which is transparent to light
5 having a wavelength within the range of 190 to 3500 nanometers, pads can be made which allow in situ end-point detection using optical methods such as interferometry.

The transparent pads can be made by any of the methods known to those skilled in the art of making polymer sheet such as casting and
10 extrusion. The polymer may be a thermoplastic material which is heated to a temperature at which it will flow and is then formed by a process such as casting or extrusion. The pad material may be a thermosetting polymer where the reactive ingredients are mixed together and heated in a mold to a temperature at which the mixture sets. If the sheet as cast meets the
15 thickness specifications desired, it may be used as is for the polishing operation. As an alternative, the pad sheet may be sliced out of the polymer as cast.

If one wishes to have just a transparent window in an otherwise opaque pad, a possible method of manufacture would be to cast a rod or a
20 plug of the transparent polymer. This casting can then be inserted in the opaque polymer in its mold while it is still liquid making sure that there is complete contact between the transparent plug and the opaque polymer. After the opaque polymer has set it may be unmolded and sheets for pads with transparent windows may be sliced from the casting.

25 As shown in U.S. Patent 5,489,233, pads useful for chemical-mechanical polishing of integrated circuit wafers which are made of a polymer sheet which has no intrinsic ability to absorb or transport slurry particles must have in use a surface texture or pattern comprising both large

and small flow channels. There will be, therefore, some interference due to the small amount of slurry in these flow channels when one makes in situ optical measurements through a transparent portion of the polishing pad. One can compensate for this interference. Since the slurry in the flow channels is relatively constant, its effect can be nulled out of the signal which is measuring the changes in the wafer surface.

It is also possible for one to insert a window into any of the types of polishing pads which are currently being used for chemical-mechanical polishing of integrated circuit wafers. Examples of these types of pads are urethane impregnated polyester felts, microporous urethane pads of the type sold as Politex by Rodel, Inc. of Newark, Delaware, and filled and/or blown composite urethanes such as IC-series and MH-series polishing pads also manufactured by Rodel, Inc. of Newark, Delaware. Such pads are not made from a solid uniform polymer sheet which has no intrinsic ability to absorb or transport slurry particles. They are by their pore-containing nature intrinsically capable of slurry transport. A hole could be cut through any of these pads and a plug of solid transparent polymer inserted to act as a window for optical end-point detection. It would be best that the surface of the solid polymer plug have a surface texture or pattern as described in U.S. Patent 5,489,233 so that polishing activity is close to being uniform over the entire polishing pad.

In addition to the polymers previously mentioned (polyurethanes, acrylics, polycarbonates, nylons and polyesters) it is possible to make a transparent window out of polyvinyl chlorides, polyvinylidene fluorides, polyether sulfones, polystyrenes, polyethylenes and polytetrafluoroethylenes. Such windows can be made by casting or extruding the polymer and then cutting the polymer to the desired size and thickness.

CLAIMS

1. A pad useful for polishing integrated circuit wafers, said pad having at least a portion comprised of a solid uniform polymer sheet with no intrinsic ability to absorb or transport slurry particles, said polymer sheet
5 being transparent to light having a wavelength within the range of 190 to 3500 nanometers.

2. A pad according to claim 1 wherein said solid uniform polymer sheet has a surface with a surface texture or pattern comprising both large and small flow channels which together permit transport of polishing slurry
10 containing particles across said surface, said surface texture or pattern being produced solely by external means upon said surface of said solid uniform polymer sheet.

3. A pad according to claim 1, wherein said pad comprises a first portion comprised of said solid uniform polymer sheet transparent to light
15 and a second portion comprised of a microporous polyurethane structure.

4. A pad according to claim 2, wherein said pad comprises a first portion comprised of said solid uniform polymer sheet transparent to light and a second portion comprised of a microporous polyurethane structure.

5. A pad according to claim 1, wherein said pad comprises a first
20 portion comprised of said solid uniform polymer sheet transparent to light and a second portion comprised of a filled or blown composite polyurethane structure.

6. A pad according to claim 2, wherein said pad comprises a first portion comprised of said solid uniform polymer sheet transparent to light
25 and a second portion comprised of a filled or blown composite polyurethane structure.

7. A pad according to claim 1, wherein said pad comprises a first portion comprised of said solid uniform polymer sheet transparent to light

and a second portion comprised of a solid uniform polymer sheet with no intrinsic ability to absorb or transport slurry particles, said second portion having a surface with a surface texture or pattern comprising both large and small flow channels which together permit said transport of polishing slurry containing particles across said surface, said surface texture being produced
5 solely by external means upon said surface of said solid uniform polymer sheet.

8. A pad according to claim 2, wherein said pad comprises a first portion comprised of said solid uniform polymer sheet transparent to light
10 and a second portion comprised of a solid uniform polymer sheet with no intrinsic ability to absorb or transport slurry particles, said second portion having a surface with a surface texture or pattern comprising both large and small flow channels which together permit transport of polishing slurry containing particles across said surface, said surface texture being produced
15 solely by external means upon said surface of said solid uniform polymer sheet.

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INTERNATIONAL SEARCH REPORT

International application No.
PCT/US96/13443

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) : B24B 1/00, 3/60, 29/00; B32B 33/00

US CL : 428/409; 51/298; 451/527, 533

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 428/409; 51/298; 451/527, 533

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 4,728,552 A (JENSEN, JR.) 01 MARCH 1988	
A	US 4,954,141 A (TAKIYAMA et al.) 04 September 1990	
A	US 5,020,283 A (TUTTLE) 04 June 1991	
A	US 5,177,908 A (TUTTLE) 12 January 1993	
A	US 5,036,015 A (SANDHU et al.) 30 July 1991	
A	US 5,081,796 A (SCHULTZ) 21 January 1992	
A	US 5,240,552 A (YU et al.) 31 August 1993	
A	US 5,337,015 A (LUSTIG et al.) 09 August 1994	

☒ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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19 DECEMBER 1996

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C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5,413,941 A (KOOS et al.) 09 May 1995	
A, P	US 5,489,233 A (COOK et al.) 06 February 1996	
A, P	US 5,534,106 A (COTE et al.) 09 July 1996	

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